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PatentIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Lap-Wai CHOW, et al.) Re: Information Disclosure
Statement
Serial No.: 10/637,848) Group: 2814
Filed: August 7, 2003) Examiner: Cao, Phat X
For: "USE OF SILICON BLOCK PROCESS) Our Ref: B-4505NP 620843-6
STEP TO CAMOUFLAGE A FALSE) Date: July 23, 2004
TRANSISTOR"
Commissioner for Patents
P.O. Box 1450
Alexandria VA, 22313-1450

Sir:

In accordance with the Applicants' duty to disclose information which may be material to the examination of this application, the undersigned respectfully requests that the Examiner consider on the merits the documents listed on the enclosed Form PTO-1449 (modified) before issuing the first Office Action on the merits. Copies of the foreign patent documents and the non-patent publications listed on the enclosed Form PTO-1449 (modified) are enclosed herewith for the Examiner's convenience. Copies of the U.S. patent documents listed on the enclosed Form PTO-1449 (modified) are not enclosed, pursuant to Deputy Commissioner Stephen G. Kunin's Pre OG Notice dated July 11, 2003, with the exception of U.S. Patent Application Nos. 10/828,022 and 10/881,286 because they have not yet been published.

The filing of this Information Disclosure Statement (IDS) shall not be construed as a representation that a search has been made (37 C.F.R. 1.97(g)), an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists.

It should be noted that the above-identified application may be related by subject matter to the following U.S. Application(s): 08/869,824, filed June 6, 1997 (now U.S. Patent No. 5,973,375); 09/696,826, filed October 25, 2000; 09/882,892, filed June 15,

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2001 (now U.S. Patent No. 6,740,942 B2); 10/132,523, filed April 24, 2002; 10/735,841, filed December 12, 2003; 10/789,261, filed February 26, 2004; 10/828,022, filed April 19, 2004; 10/881,286, filed on June 29, 2004; and 09/758,792, the inventor and filing date of which is unknown. Pursuant to 37 C.F.R. 1.56(a) and M.P.E.P. 2004, paragraph 9, the applicant brings these co-pending applications to the attention of the Examiner. The Examiner should consider this information during the prosecution of the above-identified application. However, citation of these applications does not constitute an admission that the claims of the present application are substantially similar or similar to those of the applications listed above.

Please note that the U.S. Application(s): 08/869,824, filed June 6, 1997 (now U.S. Patent No. 5,973,375); 09/696,826, filed October 25, 2000; 10/132,523, filed April 24, 2002; 10/735,841, filed December 12, 2003; 10/789,261, filed February 26, 2004; and 09/758,792, the inventor and filing date of which is unknown were previously cited in an Information Disclosure Statement (IDS) filed on April 23, 2004 for the subject application. Therefore, copies of the above-mentioned documents are not enclosed in this IDS.

This IDS is being submitted after receiving an Election/Restriction Office Action, but before receiving an Office Action on its merits, a Final Rejection or Notice of Allowance. Therefore, no official fees should be due; and this IDS should be considered on the merits. If this IDS is being submitted after the issuance of the first Office Action on the merits and before the issuance of a Final Rejection or Notice of Allowance, please contact the undersigned to authorize a payment of \$180.00 (or any other required amount), which is the fee set forth in 37 C.F.R. §

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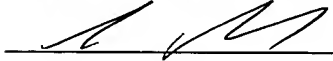
1.97(c), if the Examiner believes that such a fee is due in order for this IDS to be considered on the merits.

This paper is not being filed as a response to the Election/Restriction Office Action issued July 22, 2004. A response to that Office Action will be made with a separate filing.

The filing of this Information Disclosure Statement shall not be construed as an admission against interest in any manner. (Notice of January 9, 1992, 1135 O.G. 13-25, at 25.)

The person making this statement is the practitioner who signs below on the basis of information supplied by an individual associated with the filing and prosecution of this application (37 C.F.R. § 1.56(c)) and on the basis of information in the practitioner's file.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first-class mail in an envelope addressed to the "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450", on July 23, 2004 by Shana Morda.



Respectfully submitted,



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Enclosures: Form PTO-1449 (modified) (1 page)
Copy of Non-U.S. Patent documents listed on Form PTO-1449 (modified), with the exception of U.S. Patent Application Nos. 10/828,022 and 10/881,286



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Form PTO-1449 (Modified)	ATTY DOCKET NO. B-4505NP 620843-6	U.S. SERIAL NO. 10/637,848
LIST OF PATENTS AND PUBLICATIONS STATEMENT	APPLICANTS Lap-Wai CHOW, et al.	
	FILING DATE August 7, 2003	GROUP 2814

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUB- CLASS	FILING DATE or 102(e) DATE IF APPROPRIATE
	6,740,942 B2	5/2004	Baukus et al.	257	406	
	10/828,022		Chow et al.			4/19/2004
	10/881,286		Chow et al.			6/29/2004

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.